



shaila nalawade <shaila.nalawade@ritindia.edu>

Inputs for curriculum design- Linear Integrated Circuits and Applications Theory and Lab

shaila nalawade <shaila.nalawade@ritindia.edu>
To: yogesh mangalekar <yogeshoven@gmail.com>
Cc: satish nandawadekar <satishnandawadekar@gmail.com>

Sun, Jun 27, 2021 at 10:22 PM

Thanks for the input.

On Sun, Jun 27, 2021, 7:56 PM yogesh mangalekar <yogeshoven@gmail.com> wrote:

Dear Madam,

Thank you for your mail.

I am doing well. Hope you are also keeping well.

We have reviewed LIC syllabus and added our suggestions to include non linear circuits in Unit No-4. Also if possible please change the subject name to "Integrated Circuits & Applications" as syllabus includes both Linear & Non- Linear circuits.

Regards,
Yogesh Mangalekar

On Thu, Jun 17, 2021 at 8:11 PM shaila nalawade <shaila.nalawade@ritindia.edu> wrote:

Dear Yogesh,

Greetings!!!

Glad to interact after a long time.

Hope you are in good health and doing well.

We are in the process of curriculum Design. This course Linear Integrated Circuits and Applications Theory and Lab is for SY E & TC students. Looking at your industrial experience, on behalf of E & TC Department I request you to prove the input in designing the course Theory and Lab.

Course Title: Linear Integrated Circuits and Applications Theory

Course Title: Linear Integrated Circuits and Applications Theory Lab

PFA Syllabus for the same,

Thanks

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Prof. Mrs. S. S. Patil (Nalawade)

Asst. Professor,

Electronics & Telecommunication Engg. Department,

Cell-9970700918

Maty

HEAD,

Department of Electronics & Telecomm. Engg.

R. I. T. Rajaramnagar (Sakhrule)

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jotiram jadhav <jotiram.jadhav@ritindia.edu>

Fwd: Embedded systems Syllabus review

1 message

satyawan jagtap <satyawan.jagtap@ritindia.edu>
To: jotiram jadhav <jotiram.jadhav@ritindia.edu>

Fri, Dec 3, 2021 at 5:49 PM

----- Forwarded message -----

From: **bhagwan holkar** <bhagwan.holkar@ritindia.edu>
Date: Thu, Sep 30, 2021, 11:16
Subject: Fwd: Embedded systems Syllabus review
To: satyawan jagtap <satyawan.jagtap@ritindia.edu>

----- Forwarded message -----

From: **Vishal Tamadalage** <vishal.vbt@gmail.com>
Date: Fri, 18 Dec 2020, 1:34 am
Subject: Re: Embedded systems Syllabus review
To: bhagwan holkar <bhagwan.holkar@ritindia.edu>

Hello Sir,

The syllabus content looks good to me.

I have some suggestions in terms of using some development tools (Simulation) for coding purposes so that if we get proper results in the simulator we can move to actual hardware applications confidently.

Regards,
Vishal

On Tue, Dec 15, 2020 at 2:02 PM bhagwan holkar <bhagwan.holkar@ritindia.edu> wrote:

Dear sir

Herewith I am sending the syllabus designed for B.Tech atch 20-21. So kindly go through it and give the suggestions.
Thank you

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With best regards,
Prof. B. N. Holkar
Dept. of Electronics and Telecommunication,
Rajarambapu Institute of Technology,
Rajaramnagar, Islampur, Dist. Sangli, Maharashtra, India - 415414

Ms. S. S.
HEAD,

Department of Electronics & Telecomm. Engg.
R. I. T. Rajaramnagar (Sakhrale)
415414



jotiram jadhav <jotiram.jadhav@ritindia.edu>

Fwd: Entrepreneurship Development (ED) syllabus

1 message

Jayashree Awati(Samai) <jayashree.awati@ritindia.edu>

Fri, Dec 3, 2021 at 6:33 PM

To: satyawan jagtap <satyawan.jagtap@ritindia.edu>, jotiram jadhav <jotiram.jadhav@ritindia.edu>

----- Forwarded message -----

From: Jayashree Awati(Samai) <jayashree.awati@ritindia.edu>

Date: Tue, 22 Dec, 2020, 2:50 PM

Subject: Fwd: Entrepreneurship Development (ED) syllabus

To: sachin magdum <sachin.magdum@ritindia.edu>

Dr. J. S. Awati
ETC Department
Asst. Prof.
RIT, Islampur.

----- Forwarded message -----

From: saylee bidwai <sayleebidwai@gmail.com>

Date: Tue, Dec 22, 2020 at 2:13 PM

Subject: Re: Entrepreneurship Development (ED) syllabus

To: Jayashree Awati(Samai) <jayashree.awati@ritindia.edu>

Dear Madam, I have gone through the document. Following are my thoughts on it-

1. One of the outcomes can be "Registration of SME/MSME/STARTUP by students"
2. If possible, please add product development cycle in the contents
3. Information of latest schemes of government (atmanirbhar bharat/ startup india, etc) can be added. This will encourage the students enrolled for the course to actually avail the facilities.
4. Some assignment based on preparation of business proposal/ plan for a sample idea/ product/ project can be given to students

Hope to see budding entrepreneurs as outcome of this course!

Thank you.
Regards,
Mrs Saylee Bidwai

On Tue, Dec 22, 2020, 1:42 PM Jayashree Awati(Samai) <jayashree.awati@ritindia.edu> wrote:

Dear Madam

I am sending a syllabus. Please check the syllabus and provide suggestions.

Thank you.

Dr. J. S. Awati
ETC Department
Asst. Prof.
RIT, Islampur.

M. Atul
HEAD,

Department of Electronics & Telecomm. Engg.
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